

FIG. 5

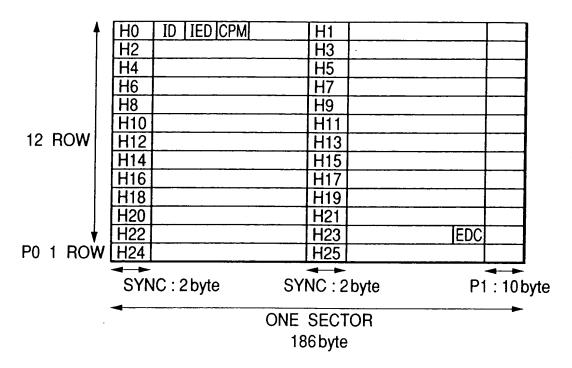


FIG. 6

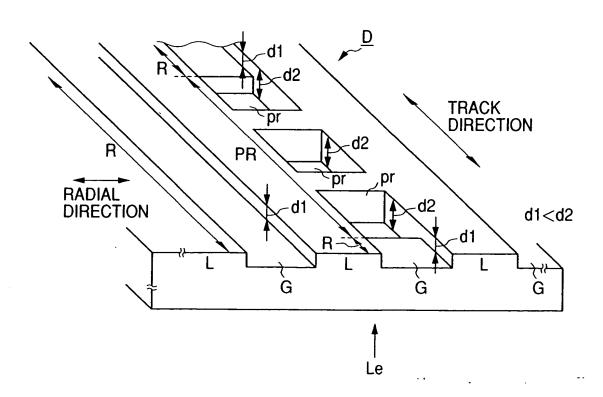


FIG. 7

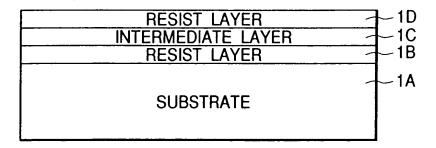
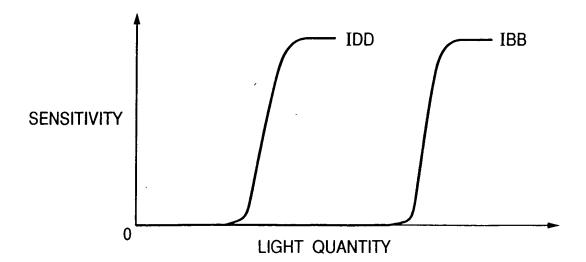
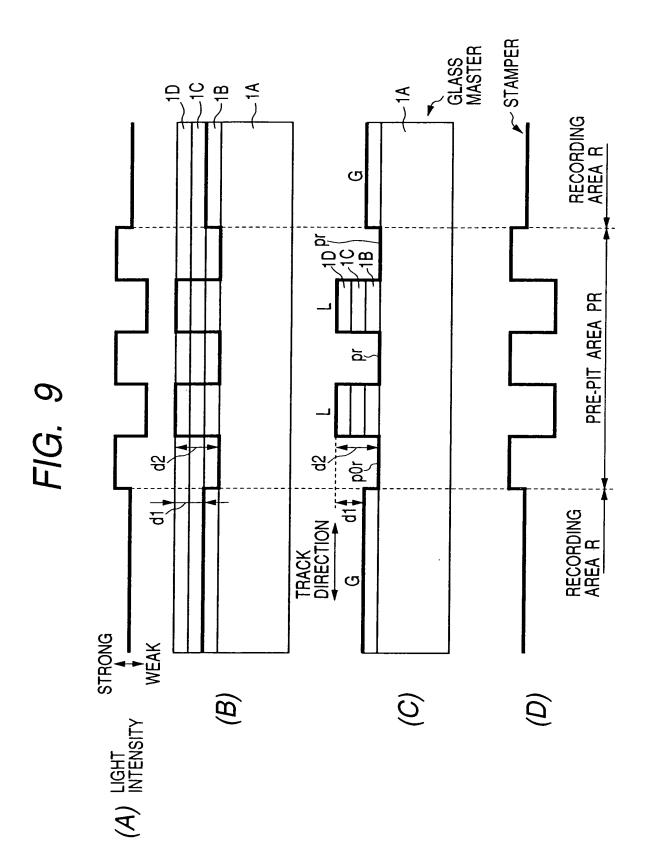


FIG. 8





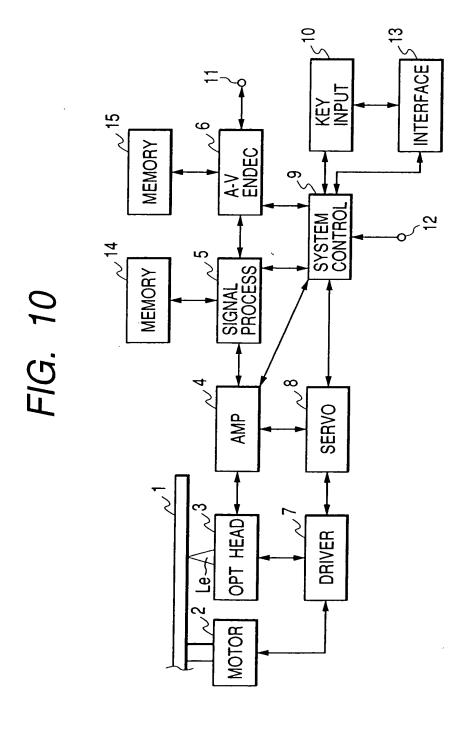


FIG. 11

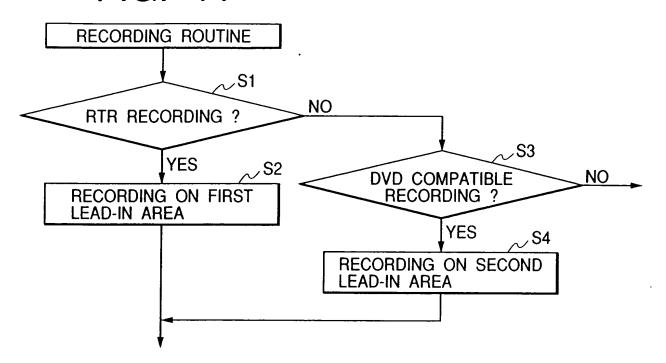


FIG. 12

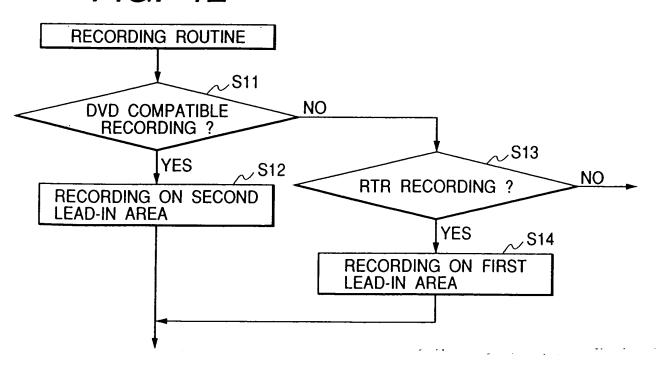


FIG. 13

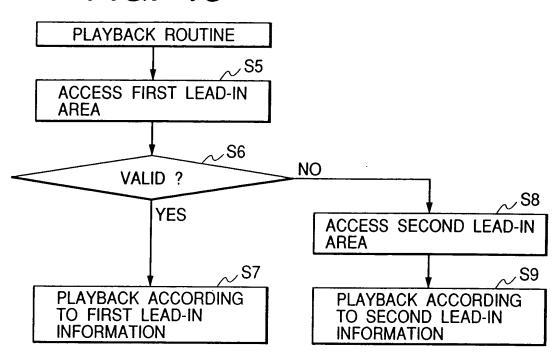


FIG. 14

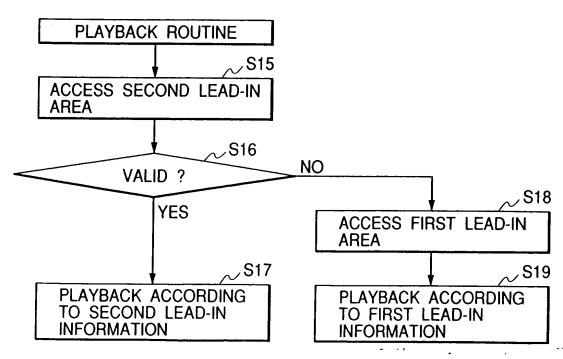


FIG. 15

ONE ECC BLOCK					 				
TIMING SIGNAL	SEC	TOR	11	11					
RECORDING AREA PRE-PIT AREA PR	R	PR	: :		 F	}			
RECORD SIGNAL									
							TIM	► E	

FIG. 16

